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**UNITED STATES DISTRICT COURT
NORTHERN DISTRICT OF CALIFORNIA**

CITY OF PROVIDENCE, individually and on
behalf of others similarly situated,

Plaintiff,

vs.

INTEL CORP., a Delaware corporation,

Defendant.

Case No. 5:18-cv-0894

CLASS ACTION COMPLAINT

DEMAND FOR JURY TRIAL

1 Plaintiff City of Providence (“Plaintiff” or “Providence”) on behalf of itself and all other similarly
2 situated, brings this action against Intel Corporation (“Defendant” or “Intel”) based upon personal
3 knowledge of the facts pertaining to itself, and upon information and belief as to all other matters, hereby
4 alleges as follows:

5 **NATURE OF THE CASE**

6 1. This is a consumer protection action seeking injunctive relief and damages arising from
7 Defendant Intel’s sale of defective microprocessor chips to Plaintiff and Class Members for over twenty-
8 three (23) years. Intel’s microprocessor chips are defective because they possess significant security
9 vulnerabilities that, if exploited, permit an adversary to access sensitive data stored elsewhere on the
10 machine or in the “cloud.” Defendant has been aware of these issues since at least June 2017, and thus
11 far, is unable to offer consumers who purchased devices containing defective microprocessor chips that
12 possess the significant security vulnerabilities described herein (“Affected Devices”) an effective
13 remedy.

14 2. Intel’s primary business is the manufacture, sale, and supply of microprocessors for
15 computer system manufacturers like Apple, Lenovo, HP, Dell, among others. Intel also manufactures
16 motherboard chipsets, network interface controllers and integrated circuits, flash memory, graphics
17 chips, embedded processors and other devices related to communications and computing. In 2016, Intel
18 reported full-year revenue of \$59.4 billion.

19 3. Intel’s marketing scheme emphasizes its cutting-edge processor speed and security.
20 Defendant repeatedly makes public representations that its machines meet certain performance metrics
21 and possess security features embedded in the hardware, which provided “robust, vulnerability-resistant
22 platforms.”¹ For example, on July 11, 2017, Intel unveiled its “powerful” new Xeon Scalable processor,
23 which broke “58 world [performance] records and counting,” and was designed to offer businesses
24 “security without compromise” while providing support to “an expanding range of existing and
25 emerging data center, and network workloads, including cloud computing, high-performance computing
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27 ¹ Data *Protection with Hardware-Assisted Security, Ensuring Data Protection Through Innovation*,
28 Intel.com, <https://www.intel.com/content/www/us/en/data-security/security-overview-general-technology.html> (last visited Jan. 31, 2018).

1 and artificial intelligence.”² Similarly on January 19, 2016, Intel unveiled its then new 6th Gen Intel
 2 Core vPro processor for “full business productivity with up to 2.5 times the performance,” which
 3 “lock[ed] the PC’s Virtual Front Door with More than Password Protection.”³

4 4. Unbeknownst to consumers purchasing Affected Devices, Defendant’s microprocessors
 5 were defectively designed, exposing Plaintiff and Class Members’ sensitive information to adversaries
 6 through at least two types of security vulnerabilities, dubbed “Meltdown” and “Spectre.”

7 5. Meltdown affects virtually every machine that runs an Intel processor, or millions of
 8 machines world-wide, as it is imbedded in nearly all of Intel’s “out-of-order” execution microprocessors
 9 manufactured since 1995.⁴ Adversaries exploiting the Meltdown flaw attack the processors “out-of-
 10 order” execution to read arbitrary kernel-memory locations, including personal data and passwords. A
 11 Meltdown attack is independent of the operating system and does not rely on software vulnerabilities,
 12 which allows it to bypass security assumptions based on address space isolation and paravirtualized
 13 environments. An adversary that uses Meltdown to infect a system may readily access and read (without
 14 user permissions or privileges) the memory of other processes in that machine or the processes of linked
 15 virtual machines (*i.e.*, those in the cloud). In addition to Microsoft and other software manufacturers
 16 releasing patches, Defendant purports to have released software patches through original equipment
 17 manufacturer (“OEM”) partners, which purport to protect 90 percent of machines affected by
 18 Meltdown.⁵ The patches, however, are not 100 percent secure and has been shown to decrease the
 19

20 ² Press Release, Intel Unveils Powerful Intel Xeon Scalable Processors, Brining Next-Generation
 21 Business and Consumer Experiences to Life, Intel.com (July 11, 2017), *available at*:
 22 [https://newsroom.intel.com/news-releases/intel-unveils-powerful-intel-xeon-scalable-processors-
 bringing-next-generation-business-consumer-experiences-life/](https://newsroom.intel.com/news-releases/intel-unveils-powerful-intel-xeon-scalable-processors-bringing-next-generation-business-consumer-experiences-life/) (last visited, Jan. 31, 2018).

23 ³ Press Release, Intel Transforms the Workplace with Latest 6th Generation Intel® Core™ vPro™
 24 Processors, Intel.com (Jan. 19, 2016), *available at*: [https://newsroom.intel.com/news-releases/intel-
 transforms-the-workplace-with-latest-6th-generation-intel-core-vpro-processors/](https://newsroom.intel.com/news-releases/intel-transforms-the-workplace-with-latest-6th-generation-intel-core-vpro-processors/) (last visited, Jan. 31,
 2018).

25 ⁴ The only known exception microprocessors that do not appear to be affected by Meltdown are Intel’s
 Itanium and pre-2013 Atoms microprocessors.

26 ⁵ Press Release, Intel Offers Security Issue Update, Intel.com (Jan. 9, 2018), *available at*:
 27 <https://newsroom.intel.com/news/intel-offers-security-issue-update/> (last visited, Feb. 7, 2018); *see*
 28 *also* Press Release, Intel Issues Updates to Protect Systems from Security Exploits, Intel.com (Jan. 4,
 2018), *available at*: [https://newsroom.intel.com/news-releases/intel-issues-updates-protect-systems-
 security-exploits/](https://newsroom.intel.com/news-releases/intel-issues-updates-protect-systems-security-exploits/) (last visited, Feb. 7, 2018).

1 performance of the Intel microprocessor by as much as 30 percent. There are also growing reports that
2 the patches are causing significant machine instability.

3 6. Spectre, meanwhile, exploits modern processor branch prediction and speculative
4 execution by instructing the microprocessor to execute the destination of a branch ahead of time and
5 then guessing the branch destination, depend on the memory value being read. The processor either
6 discards wrong speculative guesses, or if right, commits to the speculative computation when the
7 memory value finally arrives.⁶ Speculative logic, therefore, has access to the machine's memory and
8 registers and performs operations.⁷ Spectre exploits this access. In a successful attack, the adversary
9 induces the victim to "speculatively perform operations that would not occur during correct program
10 execution and which leak the victim's confidential information via a side channel to the adversary."⁸
11 Because Spectre accesses memory registers and performs software operations, securing devices affected
12 by Spectre requires each individual software vendor update its potentially vulnerable applications.
13 Spectre, therefore, is a difficult fix, and a problem that "will haunt us for quite some time."⁹

14 7. Defendant's microprocessors are defective because they expose sensitive consumer data
15 to adversaries through the Meltdown and Spectre security vulnerabilities. Moreover, Intel has thus far
16 been unable to offer consumers who purchased Affected Devices an effective repair or alternative
17 solution. Intel itself admits that patches released for Meltdown and Spectre has caused instability in
18 both newer and older machines.¹⁰ Based on these issues, Intel has even gone so far as to advise
19 consumers to stop installing current versions of its Spectre and Meltdown patches.¹¹ Defendant's defect
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21 ⁶ P. Kocher et al., *Spectre Attacks: Exploiting Speculative Execution*, available at:
22 <https://spectreattack.com/spectre.pdf> (last visited, Jan. 31, 2018).

23 ⁷ *Id.*

24 ⁸ *Id.*

25 ⁹ Meltdown and Spectre, Vulnerabilities in modern computers leak passwords and sensitive data, Graz
University of Technology, available at: <https://spectreattack.com/#faq-fix> (last visited, Jan. 31, 2018).

26 ¹⁰ D. Allan, *Intel Admits that Meltdown and Spectre patch problems affect newer CPUs*, Techradar
27 (Jan. 18, 2018), available at: <http://www.techradar.com/news/intel-admits-that-meltdown-and-spectre-patch-problems-affect-newer-cpus> (last visited, Feb. 7, 2018).

28 ¹¹ N. Shenoy, *Root Cause of Reboot Issue Identified; Updated Guidance for Customers and Partners*,
Intel.com (Jan. 22, 2018), available at: <https://newsroom.intel.com/news/root-cause-of-reboot-issue-identified-updated-guidance-for-customers-and-partners/> (last visited, Feb. 7, 2018).

1 microprocessors exist in nearly every Intel central processing unit (CPU) manufactured in the last 23
2 years, and thus, Affected Devices include most personal computers, laptops, smartphones, tablets, and
3 servers in use today.

4 8. Consumers, including Plaintiff and all members of the proposed Class, are consequently
5 left between a rock and a hard place, forced to choose between: purchasing a new machine with a
6 processor that does not contain the design defect; continuing to use Affected Devices with significant
7 security vulnerabilities; or utilizing a “patched” machine that is not 100 percent secure, which also may
8 suffer from significant performance degradation or other instability issues.

9 9. Intel’s conduct deprived consumers of the ability to make a meaningful choice from
10 among competing processor products. Had consumers known of Intel’s defectively designed processors
11 prior to purchase, consumers likely would have opted to purchase AMD or ARM processors, which are
12 not affected by the Meltdown flaw and are often priced below comparable Intel processors.

13 10. Plaintiffs and the Class it seeks to represent are consumers who purchased Affected
14 Devices. This lawsuit is brought to challenge Intel’s unfair business practices and practices pursuant to
15 the consumer protection laws of Rhode Island, R.I. Gen. Laws § 6-13.1-2, and California, Bus. & Prof.
16 Code. § 17200 and Civil Code § 1790. Plaintiff also brings a claim for: breach of express and implied
17 warranty of marketability; unjust enrichment; and negligence.

18 11. Plaintiff requests the Court find Intel’s business practices constitute unfair business
19 practices and enjoin Intel from selling affected machines in the future. Plaintiff further requests the
20 Court order Defendant to: pay civil penalties pursuant to Cal. Bus. & Prof. Code § 17206 and R.I. Gen.
21 Laws § 6-13.1-8; provide restitution to the Class of all money that may have been acquired by means of
22 their unfair practices; award punitive damages to the extent they are justified; and pay attorneys’ fees
23 and costs of litigation.

24 **JURISDICTION AND VENUE**

25 12. This Court has jurisdiction over this action under the Class Action Fairness Act, 28 U.S.C.
26 § 1332(d). The aggregated claims of the individual class members exceed the sum value of \$5,000,000,
27 exclusive of interests and costs, and this is a class action in which more than two-thirds of the proposed
28 plaintiff class, on the one hand, and Defendant, on the other, are citizens of different states.

1 13. This Court has personal jurisdiction over Defendant Intel Corp. because Intel's principle
2 place of business is located in Santa Clara, California.

3 14. Venue is proper in this district under 28 U.S.C. § 1391 because Intel resides in this District
4 and because a substantial portion of the actions and unfair practices described in this complaint, giving
5 rise to Plaintiff's claims, were conducted in and orchestrated from this District by Intel, including Intel's
6 design and marketing of the affected products. Moreover, Intel's wrongful actions harmed consumers
7 residing in this District who purchased affected products in this District.

INTRADISTRICT ASSIGNMENT

8
9 15. Assignment is proper to the San Jose division of this District under Local Rule 3-2(c)-(e),
10 as a substantial part of the events or omissions giving rise to Plaintiff's claims occurred in Santa Clara
11 County, where Intel is headquartered.

PARTIES

12
13 16. Plaintiff City of Providence is a municipal corporation with its principal address at
14 Providence City Hall, 25 Dorrance Street, Providence, Rhode Island 02903. Providence has
15 approximately 1400 municipal employees in over 35 departments. During the Class Period, Providence
16 purchased hundreds of Affect Devices, including over 800 computers with defective Intel chips in the
17 past five years.

18 17. Defendant Intel Corp., is a Delaware corporation with its principle place of business and
19 corporate headquarters at 2200 Mission College Blvd. Santa Clara, CA 95052. At all relevant times,
20 Intel was engaged in the business of designing, manufacturing, distributing, or selling electronic
21 machine products, including the defective Intel microprocessors at issue.

FACTUAL ALLEGATIONS

22
23 18. The microprocessor is the CPU of a machine or computer system, processing system data
24 and controlling other devices in the system. Founded in 1968, Intel created the world's first commercial
25 microprocessor chip in 1971. Intel remains the top-selling microprocessor manufacturer, commanding
26 in 2011, 79.3 percent of the personal computer microprocessor market and 84.4 percent of the mobile
27 personal computer (laptop) market.
28

1 19. Intel offers microprocessors with one or multiple processor cores. Multi-core processors
 2 enable improved multi-tasking and energy-efficient performance by distributing computing tasks across
 3 two or more cores. Many of Intel's processor families integrate graphics functionality with the
 4 processor. Intel offers consumers a range of platforms based upon the following microprocessors:

- 5 • Intel® Quark™ Processor: Designed to integrate with applications where lower
 6 power, size, and cost take priority, including wearable technologies and the “next
 7 generation” of intelligent, connected devices;
- 8 • Intel® Atom™ x3, x5, and x7 Processors: Designed to deliver performance and
 9 mobility in tablets, and 2 in 1 systems and smartphones, as well as power-efficiency
 10 in microservers;
- 11 • Intel® Pentium® Processor: Designed to deliver quality, reliability and performance
 12 for work and play;
- 13 • Intel® Celeron® Processor: Designed to deliver quality, reliability, and performance
 14 for work and play;
- 15 • Intel® Core™ m3 Processor: Designed to deliver performance and mobility in thin,
 16 sleek, fanless devices
- 17 • Intel® Core™ i3 i5 and i7 Processors: Designed to deliver maximum performance
 18 and built-in security for the most demanding applications;
- 19 • Intel® Xenon® Processor: Designed to deliver advance performance and energy
 20 efficiency for cost effective solutions that scale to address diverse compute, network,
 21 and storage requirements;
- 22 • Intel® Xeon Phi™ Processor: Designed to deliver optimized performance for highly
 23 parallel workloads; and
- 24 • Intel® Titanium® Processor: Designed to deliver mainframe reliability and
 25 enterprise performance on a platform sharing common characteristics of the rest of
 26 the data center.

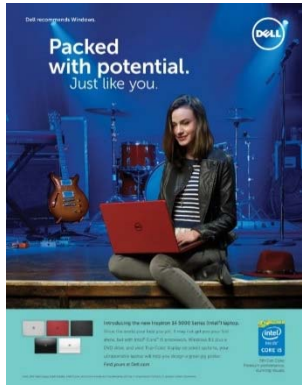
27 20. Intel's microprocessors are found in a wide variety of products, including personal
 28 computer systems manufactured by Apple, Lenovo, HP, Dell, and Acer.¹² Intel ensures consumers
 purchasing such machines are aware that the machine is being run by an Intel processor. For example:

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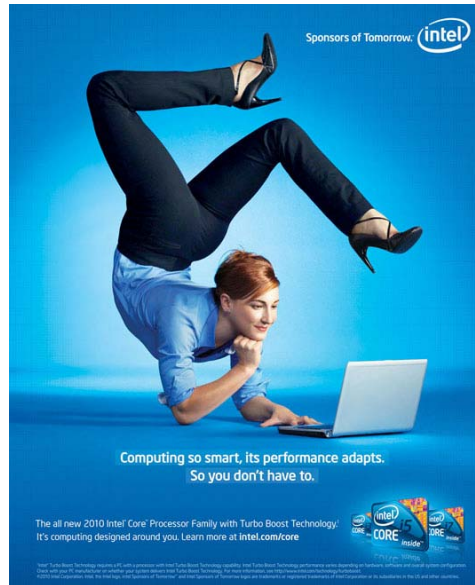
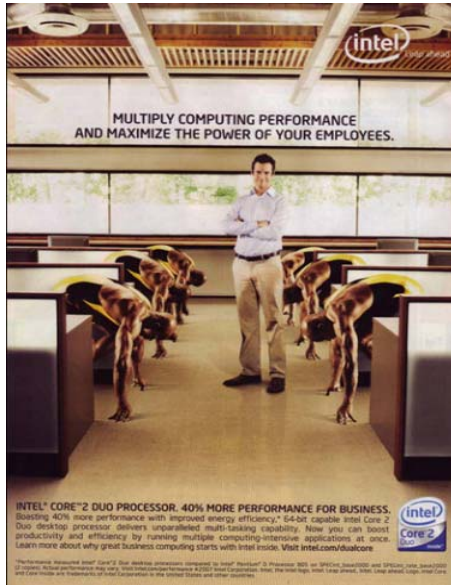
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¹² In 2016, Dell accounted for 15 percent of Intel's total revenues while Lenovo accounted for 13 percent of total revenues and HP Inc. accounted for 10 percent of total revenues.



21. Moreover, for years, Intel has touted the performance of its processors in its marketing materials. For example, Intel broadly offers that “[w]hether you want the mobility of a tablet, the versatility of a 2 in 1, or the simplicity of an All-in-One desktop, Intel® processor technology delivers the powerful performance you need, for work and for play.”¹³ The performance of its processors is described as “stunning” or “[u]nlock[ing] your full potential.”¹⁴ As seen below, Intel advertisements similarly focus on processor speed and performance:



¹³ *Performance Designed for the Way You Work and Play*, Intel.com, <https://www.intel.com/content/www/us/en/benchmarks/intel-product-performance.html> (last visited, Jan. 31, 2018).

¹⁴ *Id.*

22. Similarly, Intel regularly touts the security of its processors in its marketing materials. For example, Intel advertises that its processors offer “Data Protection with Hardware-assisted Security” and ensures “data protection through innovation.”¹⁵ In one instance, Intel emphasizes a “key component” of its approach to security is “providing more robust, vulnerability-resistant platforms. Security features are embedded in the hardware of Intel® processors, including three of Intel’s newest server processors – the Intel® Xeon® processor E3 v3 family, the Intel® Xeon® processor E5 family, and the Intel® Xeon® processor E7 family, as well as the latest generation Intel® Core™ vPro™ processors.” As seen below, Intel’s advertisements routinely focus on security measures built into its processors.



Enjoy amazing computing experiences and more control over your personal content and information with an Intel® Core™ processor-based desktop PC.

6th gen Intel™ Core™ processors with **hardware-based security features** help keep your system and data free from **malware, hacking, viruses, and prying eyes.**



GET PEACE OF MIND knowing all of your pictures, videos, and personal files are **securely stored at home** on a high-capacity hard drive.

The latest desktops with built-in, hardware-enhanced security help you stay ahead of emerging security threats while delivering amazing home computing experiences and safeguarding your personal files and data.¹

Fast end-to-end data encryption keeps your information secure without slowing you down.



Built-in hardware authentication provides seamless protection for **more secure online transactions.**



PASS823DOG...???

Say goodbye to passwords and log in to your desktop and accounts with your face, protected PIN, or a trusted second device.

Rediscover desktop computing: intel.com/desktops

1. Intel technologies' features and benefits depend on system configuration and may require enabled hardware, software or service activation. Performance varies depending on system configuration. No computer system can be absolutely secure. Check with your system manufacturer or retailer or learn more at intel.com. Copyright © 2016, Intel Corporation. All rights reserved. Intel, the Intel logo, Intel Core, the Intel Experience What's Inside logo, and Intel Experience What's Inside are trademarks of Intel Corporation in the U.S. and other countries. Other names and brands may be claimed as the property of others.

23. In June 2017, Intel learned its microprocessors suffered from several defects that allowed adversaries to access secure consumer data. These defects, colloquially known as Meltdown and Spectre, rendered Affected Devices unfit for their intended use.

¹⁵ *Data Protection with Hardware-Assisted Security, Ensuring Data Protection Through Innovation*, Intel.com, <https://www.intel.com/content/www/us/en/data-security/security-overview-general-technology.html> (last visited, Jan. 31, 2018).

1 24. Intel initially sought to keep this information secret by imposing an “information
2 embargo.” The embargo was lifted January 2, 2018.

3 25. On January 3, 2018 Intel confirmed the presence of the design defects. It was later
4 revealed the security flaws were discovered and reported by groups of independent researchers.¹⁶

5 **A. The Meltdown Security Flaw**

6 26. Meltdown affects nearly all machines using Intel microprocessors.¹⁷ Meltdown exploits
7 normal programs to read the contents of private kernel memory. Kernels are the most fundamental part
8 of an operating system, used to manage the operation of the machine’s memory and CPU time. A kernel
9 is best understood as the program controlling all other programs in a machine. Machines never interact
10 directly with the kernel, which runs behind-the-scenes unnoticed (except for the text logs it prints).

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14 ¹⁶ Independent teams working on Meltdown include: Jann Horn at Google Project Zero; Werner Hass
15 and Thomas Prescher at Cyberus Technology; and Daniel Gruss, Moritz Lipp, Stefan Mangard, and
16 Michael Schwarz at Graz University of Technology. Independent teams working on Spectre include:
17 Jann Horn at Google Project Zero; Paul Kocher in collaboration with Daniel Genkin at the University
18 of Pennsylvania and University of Maryland, Mike Hamburg at Rambus, Moritz Lipp at Graz University
19 of Technology, and Yuval Yarom at the University of Adelaide and Data61.

20 ¹⁷ The microprocessors affected include: Intel® Core™ i3 processor (45nm and 32nm), Intel® Core™
21 i5 processor (45nm and 32nm), Intel® Core™ i7 processor (45nm and 32nm), Intel® Core™ M
22 processor family (45nm and 32nm), 2nd generation Intel® Core™ processors, 3rd generation Intel®
23 Core™ processors, 4th generation Intel® Core™ processors, 5th generation Intel® Core™ processors,
24 6th generation Intel® Core™ processors, 7th generation Intel® Core™ processors, 8th generation
25 Intel® Core™ processors, Intel® Core™ X-series Processor Family for Intel® X99 platforms, Intel®
26 Core™ X-series Processor Family for Intel® X299 platforms, Intel® Xeon® processor 3400 series,
27 Intel® Xeon® processor 3600 series, Intel® Xeon® processor 5500 series, Intel® Xeon® processor
28 5600 series, Intel® Xeon® processor 6500 series, Intel® Xeon® processor 7500 series, Intel® Xeon®
Processor E3 Family, Intel® Xeon® Processor E3 v2 Family, Intel® Xeon® Processor E3 v3 Family,
Intel® Xeon® Processor E3 v4 Family, Intel® Xeon® Processor E3 v5 Family, Intel® Xeon®
Processor E3 v6 Family, Intel® Xeon® Processor E5 Family, Intel® Xeon® Processor E5 v2 Family,
Intel® Xeon® Processor E5 v3 Family, Intel® Xeon® Processor E5 v4 Family, Intel® Xeon®
Processor E7 Family, Intel® Xeon® Processor E7 v2 Family, Intel® Xeon® Processor E7 v3 Family,
Intel® Xeon® Processor E7 v4 Family, Intel® Xeon® Processor Scalable Family, Intel® Xeon Phi™
Processor 3200, 5200, 7200 Series, Intel Atom® Processor C Series, Intel Atom® Processor E Series,
Intel Atom® Processor A Series, Intel Atom® Processor x3 Series, Intel Atom® Processor Z Series,
Intel® Celeron® Processor J Series, Intel® Celeron® Processor N Series, Intel® Pentium® Processor
J Series, Intel® Pentium® Processor N Series (the “Affected CPUs”). *See Facts About the New Security
Research Findings and Intel Products*, Intel.com,
[https://www.intel.com/content/www/us/en/architecture-and-technology/facts-about-side-channel-
analysis-and-intel-products.html](https://www.intel.com/content/www/us/en/architecture-and-technology/facts-about-side-channel-analysis-and-intel-products.html) (last visited, Jan. 4, 2018).

1 27. The central security feature of modern operating systems is memory isolation; the
2 prevention of one application from accessing other application memory, or reading or writing kernel
3 memory. Isolation “is a cornerstone of [the] computing environment and allows running multiple
4 applications on personal devices or executing processes of multiple users on a single machine in the
5 cloud.”¹⁸

6 28. Meltdown overcomes memory isolation by enabling any user process to read the entire
7 kernel memory of the machine, including all physical memory mapped in the kernel region, without
8 exploiting any software vulnerability.

9 29. Meltdown exploits side-channel information available on most modern Intel
10 microarchitectures through out-of-order execution. Out-of-order execution is used to significantly
11 increase performance of processors by utilizing latencies of busy execution units. For example, instead
12 of a memory fetch unit waiting for data arrival from memory, out-of-order execution looks ahead and
13 schedules subsequent operations – *i.e.*, running machine operations out of order. Traditionally, out-of-
14 order executions were *not* thought to present security issues because the processor discards the results
15 of the memory it looks up if the instructions should not have been executed.

16 30. In June 2017, independent researchers discovered that out-of-order memory lookups
17 influence the machine’s cache, which may be detected through the cache side channel. Meltdown
18 exploits the cache side channel to access the entire kernel memory through the out-of-order execution
19 stream. The information may then be transmitted and reconstructed.

20 31. Meltdown may affect the entire physical memory on all the three major operating systems:
21 Windows, Linux, and OS X.

22 32. In January 2018, Intel announced it has been working with its OEM partners since early
23 December 2017 to release patches that cover 90 percent of machines sold in the last five (5) years. The
24 patch relies on the countermeasure KAISER, variations of which have been applied to all three operating
25 systems. However, because Meltdown bypasses the hardware-enforced isolation of security domains,
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28 ¹⁸ Moritz Lipp *et. al*, *Meltdown*, available at: <https://meltdownattack.com/meltdown.pdf> (last visited, Jan. 31, 2018).

1 the KAISER software patch still leaves small amounts of memory exposed – the design of the x86
2 architecture permits several privileged memory locations to be mapped in user space.¹⁹ While these
3 memory locations do not possess “secrets,” they might contain “pointers.” Certain pointers may be used
4 to again mount a side channel attack, breaking the kernel address space layout randomization
5 (“KASLR”).

6 33. To protect against leaking these pointers, KAISER uses a “trampoline function” –
7 randomizing the kernel with a different offset than the remaining kernels. To be effective, however,
8 trampoline code is required for every kernel memory mapped in user space. Thus, the proposed security
9 fix significantly depreciates machine performance by as much as 30 percent by protecting each kernel
10 memory with its own trampoline code. Machine slowdown has been confirmed by Microsoft.

11 34. Moreover, because certain information continues to remain exposed, and because
12 trampoline code is not foolproof, some experts suggest the only means to truly secure machine memory,
13 and thereby defeat Meltdown, is to design completely new hardware or implement a microcode update.
14 At this time, Intel has not announced it will remedy defective products through replacing hardware or
15 implementing a microcode update.

16 **B. The Spectre Security Flaw**

17 35. Researchers believe that almost every computing system, including laptops, desktops,
18 smartphones, and cloud servers, is affected by the Spectre bug.²⁰

19 36. Spectre is a related microarchitectural attack that exploits side channels to extract
20 otherwise secret unavailable information. Microprocessors use “speculative execution” to increase
21 performance by creating a checkpoint and then guessing (and prematurely executing) likely future
22 execution paths. But for speculative execution, the microprocessor would be “idling” and waiting for
23 the machine’s memory to catch up.

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27 ¹⁹ *Id.*

28 ²⁰ S. Larson, *Major chip flaws affect billions of devices*, CNNTech (Jan. 4, 2018) available at:
<http://money.cnn.com/2018/01/03/technology/computer-chip-flaw-security/index.html> (last visited,
Jan. 31, 2018).

1 37. When the direction of control flow eventually arrives from the machine’s memory, the
2 processor checks the initial guess. If the guess is wrong, the processor discards the speculative execution
3 by reverting the register state back to the stored checkpoint. The performance of the machine in such
4 situations is comparable to “idling.” However, if the processor guesses correctly, the speculative
5 execution results are committed, resulting in significant performance gains.

6 38. Previously, speculative execution was not thought to give rise to any security implications
7 because the processors are designed to revert the results of an incorrect speculative execution to their
8 prior state to maintain correctness. Spectre, however, exploits speculative execution by tricking the
9 microprocessor into speculatively executing instruction sequences that should *not* be executed during
10 correct program execution.²¹ Alternatively, Spectre may exploit speculative execution by executing
11 previous sequences as “transient instruction” to read the entire memory address space of the machine.²²
12 Finally, Spectre attacks may be used to violate JavaScript in a website to extract login cookies for other
13 sites from the browser’s memory. In any of those three cases, Spectre allows an adversary to access
14 sensitive security information across security domains.

15 39. Spectre, unlike Meltdown, cannot be fixed by a KAISER patch because an adversary may
16 use the speculative execution of other programs and machines to exploit interactions between machines.
17 Thus, the software of all programs must be updated to avoid being attacked by other programs. Intel
18 has recently informed consumers and partners to stop installing recently released patches because the
19 patches are causing devices to experience “higher than expected reboots and other unpredictable system
20 behavior.” Intel’s patch issues have trickled down to other manufacturers and developers. VMWare, a
21 cloud infrastructure company, issued a statement that it would delay microcode updates to resolve
22 coordination issues between hardware and low-level software because of problems with Intel’s firmware
23 patches.²³ Similarly, Lenovo announced that due to stability concerns, it had to recall some firmware

24
25 ²¹ P. Kocher et. al. *Spectre Attacks: Exploiting Speculative Execution*, available at:
26 <https://spectreattack.com/spectre.pdf> (last visited, Jan. 31, 2018).

27 ²² *Id.*

28 ²³ L. Newman, *Meltdown and Spectre patching has been a total train wreck*, Wired.com (Jan. 23,
2018), available at: <https://www.wired.com/story/meltdown-spectre-patching-total-train-wreck/> (last
visited, Feb. 7, 2018).

1 patches issued while Dell instructed consumers to “revert back to a previous BIOS version.”²⁴ Linux
 2 creator Linus Torvalds has called the Intel patches for the Linux kernel “COMPLETE AND UTTER
 3 GARBAGE . . . They do things that do not make sense.”²⁵

4 **C. Class Harm**

5 40. Intel’s security flaws are material and significant. The number of Affected Devices is in
 6 the millions and is thought to date back to machines manufactured in 1995. Plaintiff and Class Members
 7 would not have purchased any iterations of Affected Devices (or would not have purchased Affected
 8 Devices at the prices they did) had they known data stored on those machines may be compromised.

9 41. Competing processors, like AMD, are not affected by Meltdown. Moreover, AMD chips
 10 “are generally cheaper than comparable Intel chips.”²⁶ For example, low-end, dual core AMD Sempron,
 11 Athlon, or A-series dual-core processors cost about \$30, compared to low-end Intel chip, like the G3930
 12 dual-core processor, which costs around \$40.²⁷ Meanwhile, the price difference may be even more
 13 drastic for a higher-end processor: AMD’s eight-core Ryzen 7 1700 costs \$460 while Intel’s cheapest
 14 eight-core processor, the Core i7-7820X costs \$600.²⁸

15 42. There is also no proposed cure to Affected Devices. Researchers suggest that because the
 16 security flaws reflect fundamental hardware defects, the only certain, 100 percent fix is through a recall.
 17 Intel has not offered a recall for devices affected by either Spectre or Meltdown.

18 43. Intel even admits that the Meltdown security flaws may only be “mitigated” by software
 19 or firmware upgrades – the remedy it has proposed.²⁹ Intel also admits that patches released to address
 20 both the Spectre and Meltdown issue impacts performance of Affected Devices, causes significant
 21 machine instability, or both. Independent analyses suggest the slowdown to Affected Devices might be
 22

23 ²⁴ *Id.*

24 ²⁵ *Id.* (emphasis in original).

25 ²⁶ M. Smith, *AMD v. Intel: How does tech’s oldest rivalry look in 2018?* Digital Trends (Jan. 22, 2018)
 26 available at: <https://www.digitaltrends.com/computing/here-we-explain-the-basic-differences-between-intel-and-amd-cpus/> (last visited Jan. 31, 2018).

27 ²⁷ *Id.*

28 ²⁸ *Id.*

29 ²⁹ *Id.*

1 as high 30 percent, depending on the task and processor model.³⁰ Microsoft has also confirmed that
2 updates that seek to address these issues affect device performance.

3 44. Intel does not challenge the presence of the microprocessor design defects or that
4 Meltdown and Spectre expose Affected Devices to critical security vulnerabilities.

5 45. Intel's design defect constitutes an unfair trade practice under Rhode Island and California
6 law. Intel's design defect also constitutes a violation breach of express and implied warranties of
7 marketability, unjust enrichment, and negligence. Plaintiffs and members of the proposed Class have
8 been directly and proximately harmed as a result of Intel's actions.

9 **CLASS ACTION ALLEGATIONS**

10 46. Plaintiff brings this action pursuant to the Federal Rules of Civil Procedure Rule 23 ("Rule
11 23") on behalf of itself and others similarly situated persons or entities (the "Class"), as defined below:

12 All persons or entities in the United States who purchased or leased an
13 Affected Device from: Intel; an authorized retail seller; or from a computer
14 retail or computer manufacturer who installed an affected Intel processor
inside the Affected Device

15 47. Plaintiff also brings this action pursuant to Rule 23 on behalf of itself and the following
16 subclass for Class Members in Rhode Island (the "Rhode Island Subclass"):

17 All persons or entities in the Rhode Island who purchased or leased an
18 Affected Device from: Intel; an authorized retail seller; or from a computer
19 retail or computer manufacturer who installed an affected Intel processor
inside the Affected Device.

20 48. The nation-wide Class and the Rhode Island Subclass shall be collectively referred to as
21 the Class throughout the complaint.

22 49. Excluded from the proposed class are Defendant; any entity in which Defendant has or
23 had a controlling interest; any of Defendant's officers, directors, legal representatives, heirs, successors,
24 and assigns; Plaintiff's counsel and anyone employed by Plaintiff's counsel; any Judge assigned to this
25 action and his or her immediate family; and anyone who timely requests exclusion from the class.

26
27
28 ³⁰ J. Leyden & C. Williams, *Kernel-memory-leaking Intel processor design flaw forces Linux, Windows
redesign*, The Register (Jan. 2, 2018), available at:
https://www.theregister.co.uk/2018/01/02/intel_cpu_design_flaw/ (last visited Jan. 31, 2018).

1 50. This action may be maintained on behalf of the class proposed above under the criteria of
2 Rule 23 of the Federal Rules of Civil Procedure.

3 51. **Numerosity.** The class as presently proposed consists of owners of various affected
4 machines, described above. Upon information and belief, the number of proposed Class Member
5 exceeds tens of millions, and individual joinder of the purchasers of these machines would be
6 impractical.

7 52. **Commonality and Predominance.** Common questions of law and fact exist as to
8 members of the class and predominate over questions affecting only individual class members. These
9 common questions include:

- 10 a. Whether affected processors possess the Meltdown security flaw;
 - 11 b. Whether the affected processors possess the Spectre security flaw;
 - 12 c. The extent to which the software updates fixed Affected Devices;
 - 13 d. The extent to which the software update slowed the performance of Affected
14 Devices;
 - 15 e. Whether Defendant made any implied warranties in connection with the sale of
16 Affected Devices;
 - 17 f. Whether Defendant made any express warranties in connection with the sale of
18 Affected Devices;
 - 19 g. Whether Defendant breached any implied warranties relating to its sale of
20 Affected Devices by failing to resolve the defects in the manner required by law;
 - 21 h. Whether Defendant breached any express warranties relating to its sale of
22 Affected Devices by failing to resolve the defects in the manner required by law;
 - 23 i. Whether Defendant was unjustly enriched by the sale of Affected Devices;
 - 24 j. Whether Defendant was negligent in selling Affected Devices; and
 - 25 k. Whether Defendant violated applicable consumer protection laws by selling
26 Affected Devices or by failing to disclose the design defect, and failing to provide
27 the relief required by law.
- 28

1 53. **Typicality.** Plaintiff is a member of the proposed class and its claim is typical of the
2 claims of the other members of the class. Plaintiff and Class Members all purchased Affected Devices.
3 The injuries suffered by Plaintiff and members of the proposed Class flow from a common nucleus of
4 operative facts, as set forth above. The defenses, if any, that may be asserted against Plaintiff are similar
5 to the defenses, if any, that may be asserted against Class Members.

6 54. **Adequacy.** Plaintiff is an adequate representative of the Class because its interests do not
7 conflict with the interests of the members of the Class it seeks to represent. Plaintiff has retained counsel
8 competent and experienced in complex class action litigation, and Plaintiff intends to prosecute this
9 action vigorously. The interests of members of the Class will be fairly and adequately protected by
10 Plaintiff and its counsel.

11 55. **Superiority.** The class action device is superior to other available means for the fair and
12 efficient adjudication of the claims of Plaintiff and the Class Members. The relief sought per individual
13 member of the Class is small given the burden and expense of individual prosecution of the potentially
14 extensive litigation necessitated by the conduct of Defendant. Furthermore, it would be virtually
15 impossible for the Class Members to seek redress on an individual basis. Even if the Class Members
16 themselves could afford such individual litigation, the court system could not. Individual litigation of
17 the legal and factual issues raised by the conduct of Defendant would increase delay and expense to all
18 parties and to the court system. The Class action device presents far fewer management difficulties and
19 provides the benefits of a single, uniform adjudication, economies of scale and comprehensive
20 supervision by a single court. Given the similar nature of the Class Members' claims and the absence
21 of material differences in the state statutes and common laws upon which the Class Members' claims
22 are based, a nationwide Class will be easily managed by the Court and the parties.

23 **FIRST CAUSE OF ACTION**

24 **(For Violation of California's Unfair Competition Law**
25 **Cal. Bus. & Prof. Code § 17200, *et. seq.*)**

26 56. Plaintiff incorporates by reference the foregoing paragraphs.

27 ///

28 ///

1 57. Defendant's acts and practices, as alleged in this complaint, constitute unfair, unlawful
2 and fraudulent business practices in violation of California's Unfair Competition Law, Cal. Bus. & Prof.
3 Code § 17200, *et. seq.*

4 58. Defendant engaged and continues to engage in unfair business practices by selling
5 microprocessor chips it designed to consumers when it knew those microprocessor chips were designed
6 defectively as alleged herein. Moreover, the resolution offered by Intel for Meltdown security flaw still
7 leaves the machine vulnerable to attack and significantly reduces machine performance while also
8 causing machine instability.

9 59. Defendant's business practices were and are unscrupulous, unethical, and substantially
10 injurious to consumers. There is no legitimate business reason for Defendant's business practice such
11 that the utility of its business practice outweighs the harm to consumers. Furthermore, Defendant's
12 business practice undermines this State's fundamental policy against unfair and sharp business practices
13 that are likely to deceive or mislead consumers, and which undercuts trust and fair competition in the
14 consumer marketplace.

15 60. Plaintiff and Class Members have standing to challenge Defendant's unfair, unlawful and
16 fraudulent business practices on behalf of the public pursuant to California Business and Professions
17 Code § 17204, since it suffered injury in fact and lost money or property in the form of reduced value
18 of machine's running Intel microprocessors because of Defendant's practices. For years, Plaintiff and
19 Class Members have purchased Affected Devices.

20 61. The applicable statute of limitations for the unfair competition law claim is tolled by the
21 discovery rule and concealment.

22 62. Plaintiff and Class Members hereby seek money damages and restitution in an amount to
23 be determined at trial. Plaintiff and Class Members also seek appropriate equitable relief pursuant to
24 California Business & Professions Code § 17203, including an injunction prohibiting Defendant from
25 engaging in the same or similar unfair business practices in the future, civil penalties, restitution of
26 money that may have been acquired by Defendant's unfair business practices, and attorneys' fees and
27 costs of litigation. The entry of injunctive relief is of particular importance in this matter as it is
28 necessary to secure a fair consumer marketplace.

1 **SECOND CAUSE OF ACTION**

2 **(For Violation of the Song-Beverly Consumer Warranty Act,**
3 **Cal. Civ. Code § 1790, et. seq.)**

4 63. Plaintiff incorporates by reference the foregoing paragraphs.

5 64. Plaintiff and Class Members are “buyers,” and Defendant is a “manufacturer,”
6 “distributor,” or “retail seller” of a “consumer good” – *i.e.*, microprocessors – under Cal. Civ. Code
7 § 1791.

8 65. Every purchase or lease of Defendant’s processors or machines containing Defendant’s
9 processors to Plaintiff and Class Members included an implied warranty of merchantability whereby
10 Defendant warranted that its processors were fit for the ordinary purpose and particular purpose for
11 which they were used.

12 66. Defendant breached its implied warranty to Plaintiff and Class Members when it sold
13 Affected Devices to Plaintiff and Class Members. Defendant knew or should have known those
14 Affected Devices were not fit for the ordinary purpose and particular purpose for which they were used.
15 Specifically, microprocessors sold contained certain design defects (*i.e.*, associated security,
16 performance, and instability issues) alleged herein.

17 67. Defendant’s actions deprived Plaintiff and Class Members of the benefit of their bargain,
18 and caused Defendant’s microprocessors to be worth significantly less than what Plaintiff and Class
19 Members paid.

20 68. As a direct and proximate result of Defendant’s breach of its duties, Plaintiff and Class
21 Members received goods whose condition substantially impairs their value. Plaintiff and Class
22 Members have been damaged by the diminished value of Defendant’s microprocessors.

23 69. The applicable statute of limitations for violation of the Song-Beverly Consumer
24 Warranty Act is tolled by the nature of the latent defect described herein, the discovery rule, and
25 concealment.

26 70. Pursuant to Cal. Civ. Code §§ 1791.1 and 1794, Plaintiff and Class Members hereby seek
27 money damages in an amount to be determined at trial. Plaintiff and Class Members also seek
28 appropriate equitable relief, including: at their election, the right to revoke acceptance of Affected
Devices; overpayment; diminution in value; an injunction prohibiting Defendant from engaging in the

1 same or similar business practices in the future; and other such relief that the Court may see as just and
2 warranted. Finally, Plaintiff and Class Members seek reasonable attorney fees and costs, to which they
3 are entitled pursuant to Cal. Civ. Code § 1794.

4 **THIRD CAUSE OF ACTION**

5 **(For Violation of Rhode Island's Unfair Competition Law**
6 **R.I. Gen. Laws §§ 5-13.1-1, *et seq.*)**

7 71. Plaintiff incorporates by reference the foregoing paragraphs.

8 72. Defendant's acts and practices, as alleged in this complaint, constitute unfair, unlawful
9 and fraudulent business practices in violation of the Rhode Island Unfair Trade Practice and Consumer
10 Protection Act, R.I. Gen. Laws §§ 5-13.1-1, *et seq.*

11 73. Defendant engaged and continues to engage in unfair business practices by selling
12 Affected Devices to consumers when it knew those microprocessor chips have significant security,
13 stability, and performance issues, as alleged herein. The resolution offered by Defendant for Meltdown
14 and Spectre may still leave the machine vulnerable to attack while significantly reduces machine
15 performance and stability.

16 74. Defendant's business practices are unscrupulous, unethical, and substantially injurious to
17 consumers. There is no legitimate business reason for Intel's business practice such that the utility of
18 its business practice outweighs the harm to consumers. Furthermore, Defendant's business practice
19 undermines this State's fundamental policy against unfair and sharp business practices that are likely to
20 deceive or mislead consumers, and which undercut trust and fair competition in the consumer
21 marketplace.

22 75. Plaintiff and Class Members have standing to challenge Defendant's unfair, unlawful and
23 fraudulent business practices on behalf of the public pursuant to Rhode Island's Unfair Trade Practice
24 and Consumer Protection Act, R.I. Gen. Laws §§ 6-13.1-5.2, *et seq.*, because it suffered injury in fact
25 and lost money or property in the form of reduced value of machine's running Defendant's
26 microprocessors because of Defendant's practices. For years, Plaintiff and Class Members have
27 purchased Affected Devices.

28 76. The applicable statute of limitations for the unfair competition law claim is tolled by the
discovery rule and concealment.

1 77. Plaintiff and Class Members hereby seeks money damages and restitution in an amount
2 to be determined at trial. Plaintiff and Class Members also seek appropriate equitable relief pursuant to
3 Rhode Island's Unfair Trade Practice and Consumer Protection Act, R.I. Gen. Laws §§ 6-13.1-5.2, *et*
4 *seq.*, including an injunction prohibiting Defendant from engaging in the same or similar unfair business
5 practices in the future, civil penalties, restitution of money that may have been acquired by Defendant's
6 unfair business practices, and attorneys' fees and costs of litigation. The entry of injunctive relief is of
7 particular importance in this matter as it is necessary to secure a fair consumer marketplace.

8 **FOURTH CAUSE OF ACTION**

9 **(For Violation of the Magnuson-Moss Warranty Act, 10 15 U.S.C. § 2301, *et. seq.*)**

11 78. Plaintiff incorporates by reference the foregoing paragraphs.

12 79. The Magnuson-Moss Warranty Act ("MMWA") governs warranties on consumer
13 products, and was designed to prohibit manufacturers from using unfair or misleading warranties on
14 consumer products.

15 80. Plaintiff and Class Members are "consumers" under MMWA because they purchased or
16 leased (not for the purpose of resale) a consumer product.

17 81. Defendant is a "supplier" because it engaged "in the business of making a consumer
18 product directly or indirectly available to consumers." Defendant is also a "warrantor" under MMWA
19 because, as a supplier, it is obligated under an implied warranty. Defendant impliedly warranted that
20 Intel's chips were merchantable and fit for the ordinary and particular purpose for which they were used.

21 82. Defendant breached its implied warranty to Plaintiff and Class Members when it sold
22 Affected Devices to Plaintiff and Class Members. Defendant knew or should have known Affected
23 Devices were not fit for the ordinary purpose and particular purpose for which they were used.
24 Specifically, microprocessors sold contained certain design defects (*i.e.*, associated security, stability,
25 and performance issues) alleged herein.

26 83. Plaintiff and Class Members were directly and proximately damaged as a result of
27 Defendant's breach of the implied warranty of merchantability.

28 84. It would be unconscionable to permit Defendant to disclaim, modify, or limit its implied
warranties under MMWA. Defendant knowingly sold or leased defective products or machines

1 containing defective products. Defendant was notified by independent research teams that its products
2 were defectively designed, a fact that Defendant knew or should have known much earlier. Defendant
3 knew or should have known it was selling or leasing Affected Devices, yet it did not disclose these
4 design defects to Plaintiff or Class Members. Affording Defendant a reasonable opportunity to cure its
5 breach of implied warranties would be unnecessary and futile because Defendant knew and concealed
6 the defects alleged herein, and in any event, is unable or has refused to adequately repair or replace
7 Affected Devices at no cost to Plaintiff or Class Members.

8 85. The applicable statute of limitations for the MMWA claim is tolled by the discovery rule
9 and concealment.

10 86. Plaintiff and Class Members hereby seek money damages and restitution in an amount to
11 be determined at trial. Plaintiff and Class Members have complied with all obligations under the
12 MMWA, or have otherwise been excused from performance of such obligations due to Defendant's
13 conduct described herein.

14 87. Plaintiff and Class Members also seek appropriate equitable relief pursuant to MMWA,
15 15 U.S.C. § 2310, including an injunction prohibiting Defendant from engaging in the same or similar
16 business practices in the future, restitution of money that may have been acquired by Defendant's breach
17 of its implied warranty of merchantability, and other such relief that the Court may see as just and
18 warranted.

19 **FIFTH CAUSE OF ACTION**

20 **(For Breach of Implied Warranty)**

21 88. Plaintiff incorporates by reference the foregoing paragraphs.

22 89. The Uniform Commercial Code ("U.C.C.") § 2-314 provides for an implied warranty of
23 merchantable goods in every contract for the sale of goods between a merchant and a consumer.
24 Defendant is and at all relevant times was a "merchant" and seller of "goods," as defined by the U.C.C.

25 90. Defendant impliedly warranted that its processors were in merchantable condition and fit
26 for the ordinary purpose for which Defendant marketed its processors to be used.

27 91. Plaintiff and Class Members purchased or leased Affected Devices from Defendant.

28 ///

1 92. Defendant breached its implied warranty of merchantability. The Affected Devices sold
2 or leased by Defendant to Plaintiff and Class Members was not fit for the ordinary purpose for which
3 Defendant marketed its processors to be used due to the design defects (and associated security, stability,
4 and performance issues) alleged herein.

5 93. It would be unconscionable to permit Defendant to limit or disclaim its implied warranties
6 as it knowingly sold or leased Affected Devices. Defendant was notified by independent research teams
7 that its products were defectively designed, a fact that Defendant knew or should have known much
8 earlier. Defendant knew or should have known it was selling or leasing Affected Devices, yet it did not
9 disclose these design defects to Plaintiff or Class Members. Affording Defendant a reasonably
10 opportunity to cure its breach of implied warranties would be unnecessary and futile because Defendant
11 knew and concealed the defects alleged herein, and in any event, is unable or has refused to adequately
12 repair or replace Affected Devices at no cost to Plaintiff or Class Members.

13 94. Moreover, Defendant, as a manufacturer of consumer goods is precluded from excluded
14 or modifying an implied warranty of merchantability or limiting customer remedies for breach of this
15 warranty.

16 95. The applicable statute of limitations for the implied warranty claim is tolled by the
17 discovery rule and concealment.

18 96. Defendant's warranties were designed to influence consumers purchasing Affected
19 Devices.

20 97. As a direct and proximate result of Defendant's breach of its implied warranty of
21 merchantability, Plaintiff and Class Members were damaged in an amount to be proven at trial. Plaintiff
22 and Class Members have complied with all obligations under this warranty, or have otherwise been
23 excused from performance of such obligations due to Defendant's conduct described herein.

24 **SIXTH CAUSE OF ACTION**

25 **(For Breach of Express Warranty of Marketability)**

26 98. Plaintiff incorporates by reference the foregoing paragraphs.

27 ///

28 ///

1 99. Defendant is a “merchant” within the meaning of the U.C.C. because it designed,
2 manufactured, advertised, and distributed the defective processors at issue in this litigation. Moreover,
3 the defective processors are “goods” within the meaning of the U.C.C.

4 100. In connection with the sale or lease of Affected Devices, Defendant represented that its
5 processors were fit for the ordinary purpose for which Defendant marketed its processors to be used.
6 However, due to the design defects (and associated security, stability, and performance issues) alleged
7 herein, the processors were not fit for the ordinary purpose for which Defendant marketed its processors.

8 101. Defendant’s affirmation of facts and promises relating to its defective processors became
9 part of the bargain of sale, and created an express warranty that the processors would conform to
10 Defendant’s affirmations and promises.

11 102. Defendant’s express warranties run to Plaintiff and Class Members either directly or as
12 third-party beneficiaries.

13 103. Defendant’s breached its express warranties by selling or leasing Affected Devices to
14 Plaintiff and Class Members that did not conform to Defendant’s affirmations and promises.

15 104. The applicable statute of limitations for the express warranty claim is tolled by the
16 discovery rule and concealment.

17 105. Defendant’s breach of express warranties directly and proximately caused damages, injury
18 in fact, and ascertainable loss to Plaintiff and Class Members in an amount to be determined at trial.

19 106. All conditions precedent to this claim have been satisfied.

20 **SEVENTH CAUSE OF ACTION**

21 **(For Unjust Enrichment)**

22 107. Plaintiff incorporates by reference the foregoing paragraphs.

23 108. Defendant was and should have been reasonably expected to manufacture and sell
24 microprocessors free from the design defects alleged herein. Defendant benefited from the purchase or
25 lease of Affected Devices by Plaintiff and Class Members.

26 109. There is privity between Defendant and Plaintiff and Class Members because Defendant
27 intended Plaintiff and Class Members to be purchasers or lessors of Affected Devices.

28 ///

1 110. Defendant unjustly profited from the sale and lease of Affected Devices at inflated prices
2 due to materially deceptive advertising, marketing, false representations, omissions, and concealments.
3 But for Defendant's materially deceptive advertising, marketing, false representations, omissions, and
4 concealments, Plaintiff and Class Members would not have purchased or leased Affected Devices at the
5 prices paid. As a result, Defendant received ill-gotten gains, benefits, and profits.

6 111. Defendant has been unjustly enriched at the expense of Plaintiff and Class Members
7 because it did not provide the benefits represented. It would be inequitable for Defendant to retain those
8 ill-gotten gains, benefits, and profits.

9 112. The applicable statute of limitations for the unjust enrichment claim is tolled by the
10 discovery rule and concealment.

11 113. Due to Defendant's unlawful, unjust, and inequitable conduct, Plaintiff and Class
12 Members are entitled to restitution of an amount to be proven at trial that represents Defendant's ill-
13 gotten gains, benefits, and profits, including interest.

14 **EIGHTH CAUSE OF ACTION**

15 **(For Negligence)**

16 114. Plaintiff incorporates by reference the foregoing paragraphs.

17 115. Defendant owed a duty of care to Plaintiff and Class Members to exercise reasonable care
18 to safeguard sensitive information stored on machines purchased by consumers that utilize Defendant
19 microprocessors from foreseeable security vulnerabilities arising due to the microarchitectural design of
20 those microprocessors.

21 116. Defendant's duty to Plaintiff and Class Members included, *inter alia*, designing,
22 maintaining, monitoring, and testing microprocessors to ensure it detected microarchitectural issues,
23 such as the design defects alleged herein, in a timely manner.

24 117. In marketing and selling microprocessors to consumers, Defendant also had a duty to
25 ensure its microprocessors could reasonably function at the quality, stability, and processing speeds
26 represented.

27 118. Finally, Defendant owed a duty to disclose any material defects, like the design defect
28 alleged herein, to Plaintiff and Class Members in a timely manner.

1 119. Defendant breached each of these duties.

2 120. But for Defendant's breach of these duties, Plaintiff and Class Members would not have
3 purchased or leased Affected Devices at the prices paid.

4 121. Plaintiff and Class Members were foreseeable victims of Defendant's wrongdoing.
5 Defendant knew or should have known that its processors would cause damages to Plaintiff and Class
6 Members.

7 122. The applicable statute of limitations for the negligence claim is tolled by the discovery
8 rule and concealment.

9 123. As a direct and proximate result of Defendant's negligence, Plaintiff and Class Members
10 have been damaged in an amount to be proven at trial. Damages include, but are not limited to,
11 compensatory damages, incidental and consequential damages, punitive damages, and any other damage
12 allowed by law.

13 **PRAYER FOR RELIEF**

14 WHEREFORE, Plaintiff prays that the Court enters judgment in their favor and against Intel as
15 follows:

16 A. An order certifying the proposed class, or an alternative class that the Court may find
17 appropriate under Rule 23 of the Federal Rules of Civil Procedure, directing that Plaintiff's claims
18 proceed on a class-wide basis, and appointing Plaintiff and her counsel to represent the class;

19 B. An order and/or judgement enjoining Defendant from manufacturing microprocessor
20 chips that contain known security flaws;

21 C. An order and/or judgment requiring Defendant to make restitution to Plaintiff of money
22 that may have been acquired by means of its unfair practices;

23 D. An order granting reasonable attorneys' fees and costs, as well as pre- and post-judgment
24 interest at the maximum legal rate; and

25 ///

26 ///

27 ///

28 ///

1 E. Such other and further relief as this Court may deem appropriate.

2 Respectfully submitted,

3 DATED: February 12, 2018

KAPLAN FOX & KILSHEIMER LLP

4 By: /s/ Laurence D. King
5 Laurence D. King

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24 *Attorneys for Plaintiff*

CIVIL COVER SHEET

The JS-CAND 44 civil cover sheet and the information contained herein neither replace nor supplement the filing and service of pleadings or other papers as required by law, except as provided by local rules of court. This form, approved in its original form by the Judicial Conference of the United States in September 1974, is required for the Clerk of Court to initiate the civil docket sheet. (SEE INSTRUCTIONS ON NEXT PAGE OF THIS FORM.)

I. (a) PLAINTIFFS

CITY OF PROVIDENCE, individually, and on behalf of all others similarly situated,

(b) County of Residence of First Listed Plaintiff Providence County, RI (EXCEPT IN U.S. PLAINTIFF CASES)

(c) Attorneys (Firm Name, Address, and Telephone Number)

Laurence D. King / KAPLAN FOX & KILSHEIMER LLP 350 Sansome Street, Suite 400, San Francisco, CA 94104 (415) 772-4700; lking@kaplanfox.com

DEFENDANTS

INTEL CORPORATION

County of Residence of First Listed Defendant (IN U.S. PLAINTIFF CASES ONLY)

NOTE: IN LAND CONDEMNATION CASES, USE THE LOCATION OF THE TRACT OF LAND INVOLVED.

Attorneys (If Known)

II. BASIS OF JURISDICTION (Place an "X" in One Box Only)

- 1 U.S. Government Plaintiff 3 Federal Question (U.S. Government Not a Party) 2 U.S. Government Defendant X 4 Diversity (Indicate Citizenship of Parties in Item III)

III. CITIZENSHIP OF PRINCIPAL PARTIES (Place an "X" in One Box for Plaintiff and One Box for Defendant)

Table with columns for Plaintiff (PTF) and Defendant (DEF) citizenship and incorporation status. Includes options like 'Citizen of This State', 'Citizen of Another State', 'Citizen or Subject of a Foreign Country', 'Incorporated or Principal Place of Business In This State', etc.

IV. NATURE OF SUIT (Place an "X" in One Box Only)

Large table with categories: CONTRACT, REAL PROPERTY, TORTS, CIVIL RIGHTS, PRISONER PETITIONS, HABEAS CORPUS, OTHER, FORFEITURE/PENALTY, LABOR, IMMIGRATION, BANKRUPTCY, SOCIAL SECURITY, FEDERAL TAX SUITS, OTHER STATUTES. Each category lists specific legal codes and descriptions.

V. ORIGIN (Place an "X" in One Box Only)

- X 1 Original Proceeding 2 Removed from State Court 3 Remanded from Appellate Court 4 Reinstated or Reopened 5 Transferred from Another District (specify) 6 Multidistrict Litigation-Transfer 8 Multidistrict Litigation-Direct File

VI. CAUSE OF ACTION

Cite the U.S. Civil Statute under which you are filing (Do not cite jurisdictional statutes unless diversity): 28 U.S.C. sec. 1332(d)

Brief description of cause: Violation of the Consumer Protection Act for false representations

VII. REQUESTED IN COMPLAINT:

CHECK IF THIS IS A CLASS ACTION UNDER RULE 23, Fed. R. Civ. P. DEMAND \$ 5,000,000.00

CHECK YES only if demanded in complaint: JURY DEMAND: X Yes No

VIII. RELATED CASE(S), IF ANY (See instructions):

JUDGE Edward J. Davila DOCKET NUMBER 5:18-cv-00046-EJD

IX. DIVISIONAL ASSIGNMENT (Civil Local Rule 3-2)

(Place an "X" in One Box Only) SAN FRANCISCO/OAKLAND X SAN JOSE EUREKA-MCKINLEYVILLE

DATE 02/12/2018

SIGNATURE OF ATTORNEY OF RECORD

/s/ Laurence D. King

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INSTRUCTIONS FOR ATTORNEYS COMPLETING CIVIL COVER SHEET FORM JS-CAND 44

Authority For Civil Cover Sheet. The JS-CAND 44 civil cover sheet and the information contained herein neither replaces nor supplements the filings and service of pleading or other papers as required by law, except as provided by local rules of court. This form, approved in its original form by the Judicial Conference of the United States in September 1974, is required for the Clerk of Court to initiate the civil docket sheet. Consequently, a civil cover sheet is submitted to the Clerk of Court for each civil complaint filed. The attorney filing a case should complete the form as follows:

- I. a) Plaintiffs-Defendants.** Enter names (last, first, middle initial) of plaintiff and defendant. If the plaintiff or defendant is a government agency, use only the full name or standard abbreviations. If the plaintiff or defendant is an official within a government agency, identify first the agency and then the official, giving both name and title.
- b) County of Residence.** For each civil case filed, except U.S. plaintiff cases, enter the name of the county where the first listed plaintiff resides at the time of filing. In U.S. plaintiff cases, enter the name of the county in which the first listed defendant resides at the time of filing. (NOTE: In land condemnation cases, the county of residence of the “defendant” is the location of the tract of land involved.)
- c) Attorneys.** Enter the firm name, address, telephone number, and attorney of record. If there are several attorneys, list them on an attachment, noting in this section “(see attachment).”
- II. Jurisdiction.** The basis of jurisdiction is set forth under Federal Rule of Civil Procedure 8(a), which requires that jurisdictions be shown in pleadings. Place an “X” in one of the boxes. If there is more than one basis of jurisdiction, precedence is given in the order shown below.
- (1) United States plaintiff. Jurisdiction based on 28 USC §§ 1345 and 1348. Suits by agencies and officers of the United States are included here.
 - (2) United States defendant. When the plaintiff is suing the United States, its officers or agencies, place an “X” in this box.
 - (3) Federal question. This refers to suits under 28 USC § 1331, where jurisdiction arises under the Constitution of the United States, an amendment to the Constitution, an act of Congress or a treaty of the United States. In cases where the U.S. is a party, the U.S. plaintiff or defendant code takes precedence, and box 1 or 2 should be marked.
 - (4) Diversity of citizenship. This refers to suits under 28 USC § 1332, where parties are citizens of different states. When Box 4 is checked, the citizenship of the different parties must be checked. (See Section III below; **NOTE: federal question actions take precedence over diversity cases.**)
- III. Residence (citizenship) of Principal Parties.** This section of the JS-CAND 44 is to be completed if diversity of citizenship was indicated above. Mark this section for each principal party.
- IV. Nature of Suit.** Place an “X” in the appropriate box. If the nature of suit cannot be determined, be sure the cause of action, in Section VI below, is sufficient to enable the deputy clerk or the statistical clerk(s) in the Administrative Office to determine the nature of suit. If the cause fits more than one nature of suit, select the most definitive.
- V. Origin.** Place an “X” in one of the six boxes.
- (1) Original Proceedings. Cases originating in the United States district courts.
 - (2) Removed from State Court. Proceedings initiated in state courts may be removed to the district courts under Title 28 USC § 1441. When the petition for removal is granted, check this box.
 - (3) Remanded from Appellate Court. Check this box for cases remanded to the district court for further action. Use the date of remand as the filing date.
 - (4) Reinstated or Reopened. Check this box for cases reinstated or reopened in the district court. Use the reopening date as the filing date.
 - (5) Transferred from Another District. For cases transferred under Title 28 USC § 1404(a). Do not use this for within district transfers or multidistrict litigation transfers.
 - (6) Multidistrict Litigation Transfer. Check this box when a multidistrict case is transferred into the district under authority of Title 28 USC § 1407. When this box is checked, do not check (5) above.
 - (8) Multidistrict Litigation Direct File. Check this box when a multidistrict litigation case is filed in the same district as the Master MDL docket. Please note that there is no Origin Code 7. Origin Code 7 was used for historical records and is no longer relevant due to changes in statute.
- VI. Cause of Action.** Report the civil statute directly related to the cause of action and give a brief description of the cause. **Do not cite jurisdictional statutes unless diversity.** Example: U.S. Civil Statute: 47 USC § 553. Brief Description: Unauthorized reception of cable service.
- VII. Requested in Complaint.** Class Action. Place an “X” in this box if you are filing a class action under Federal Rule of Civil Procedure 23.
- Demand. In this space enter the actual dollar amount being demanded or indicate other demand, such as a preliminary injunction.
- Jury Demand. Check the appropriate box to indicate whether or not a jury is being demanded.
- VIII. Related Cases.** This section of the JS-CAND 44 is used to identify related pending cases, if any. If there are related pending cases, insert the docket numbers and the corresponding judge names for such cases.
- IX. Divisional Assignment.** If the Nature of Suit is under Property Rights or Prisoner Petitions or the matter is a Securities Class Action, leave this section blank. For all other cases, identify the divisional venue according to Civil Local Rule 3-2: “the county in which a substantial part of the events or omissions which give rise to the claim occurred or in which a substantial part of the property that is the subject of the action is situated.”
- Date and Attorney Signature.** Date and sign the civil cover sheet.